Burst Buffer Acceleration

Xilinx Alveo powers Eideticom NoLoad® Computational Storage

SOLUTION BRIEF

XILINX ALVEO™

- High performance
- Simpler, lower cost storage
- Minimized checkpoint time

INTRODUCTION

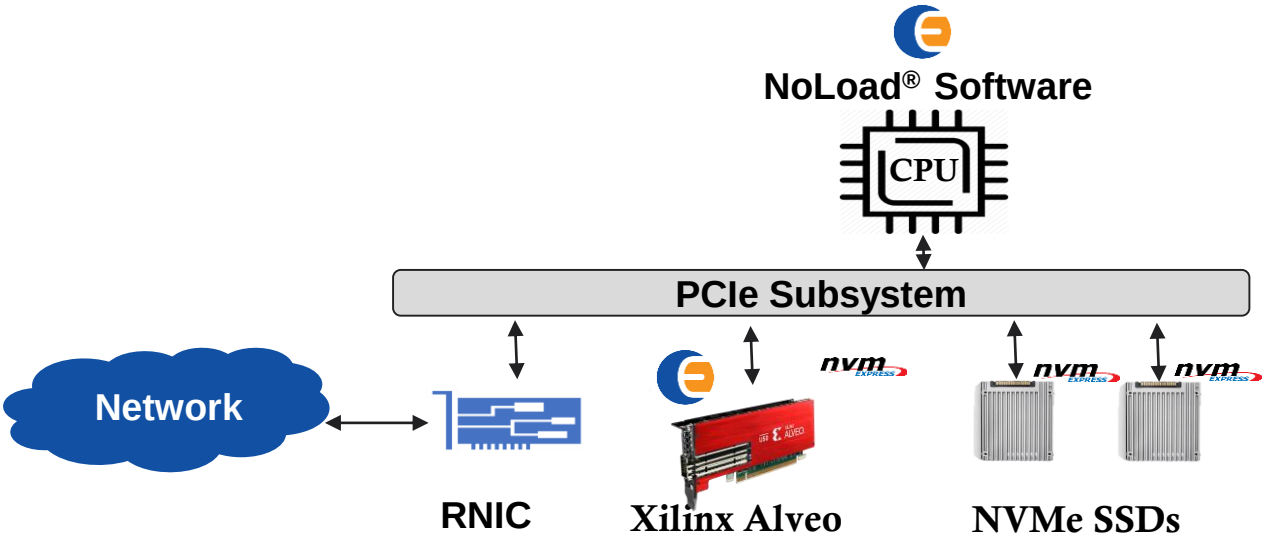
In order to keep pace with increasing scale of data and processing in HPC, burst buffers are a critical bridge between compute and back-end storage. Eideticom's NoLoad® Computational Storage accelerated by Xilinx® Alveo™ is a high performance and efficient solution for burst buffering.

KEY BENEFITS

- Reduced burst-buffer storage costs
- Minimized checkpoint time through Increased burst buffer write speed
- Reduced data movement and improved power efficiency.
- Burst buffer can be filled and drained at the same time.
- Compression computation is offloaded from the CPU

SOLUTION OVERVIEW

The Eideticom HPC Burst Buffer solution combines fast block storage and high-speed network devices with Eideticom's NoLoad® CSP and software.



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SOLUTION DETAILS & KEY BENEFITS

Add Eideticom NoLoad® Burst Buffer to any Standard Linux Server

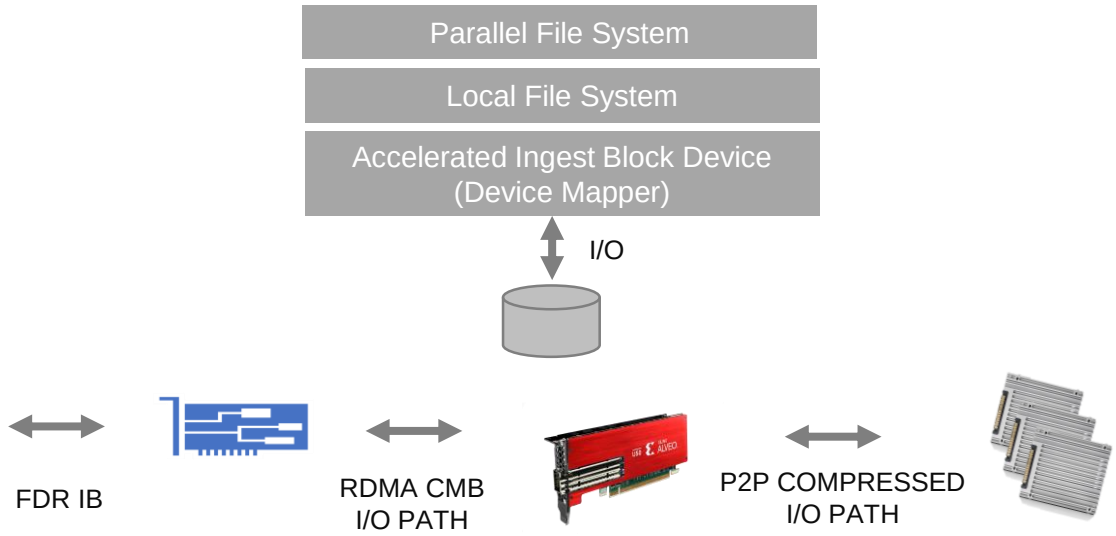
Hardware required:

- Xilinx Alveo U50, U200 or U250
- Standard NVMe SSDs.
- RDMA enabled Network Interface Card (NIC).

Software stack:

- p2pdma enabled operating system (e.g., Linux).
- Eideticom software running on host server
- NoLoad® Computational Storage Processor (CSP) software running on Xilinx Alveo.

LOGICAL VIEW / DEVICE MAPPING



TAKE THE NEXT STEP

Learn more about Alveo accelerators
Learn more about Eideticom
Reach out to Eideticom sales – sales@eideticom.com