

Product Discontinuation Notice For Development Systems Products

XCN18025 (v1.0) October 29, 2018

Product Discontinuation Notice

Overview

The purpose of this notification is to communicate that Xilinx is discontinuing the Virtex[®] UltraScale+™ VCU1525 Development Kit.

Description

As part of Xilinx introduction of the Alveo Data Center Accelerator product line, the following products have been identified for obsolescence. This notice should be used in conjunction with previous discontinuance notices which can be found at Xilinx.com.

Products Affected

Virtex UltraScale+ VCU1525 Development Kit

Table 1: Virtex UltraScale+ VCU1525 Development Kit

Part Number	Description	Replacement Part
DK-U1-VCU1525-A-G	Xilinx Virtex UltraScale+ FPGA VCU1525 Data Center Development Kit with Active Cooling	A-U200-A64G-PQ-G
DK-U1-VCU1525-P-G	Xilinx Virtex UltraScale+ FPGA VCU1525 Data Center Development Kit with Passive Cooling	A-U200-P64G-PQ-G

Key Dates and Ordering Information

Final orders accepted until inventory is exhausted or November 30, 2018; whichever occurs first. Final shipments will be until December 14, 2018.

Response

Customers are requested to place last-time-buy orders in a timely manner prior to the key dates listed above to avoid product availability issues. If you anticipate that your last time buy requirements will exceed the current rate, please provide your Xilinx Sales Representative with a forecast of the requirements. All orders will be supported on a first-come, first-serve basis. All orders placed within 60 days of the last time buy date will be non-cancellable and non-returnable (NCNR). For additional information or questions, contact your local Xilinx Sales representative.

Important Notice: Xilinx Customer Notifications (XCNs, XDNs, and Quality Alerts) can be delivered via e-mail alerts sent by the Support website (https://www.xilinx.com/support). Register today and personalize your "Documentation and Design Advisory Alerts" area to include Customer Notifications. Xilinx Support provides many benefits, including the ability to receive alerts for new and updated information about specific products, as well as alerts for other publications such as data sheets, errata, application notes, etc. For information on how to sign up, refer to Xilinx Answer Record 18683.

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Revision History

The following table shows the revision history for this document.

Date	Version	Revision
10/29/18	1.0	Initial release.

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