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Alveo™ U55C Accelerator Card

OVERVIEW

Next-generation HPC applications need to do more with each watt: squeeze more out of each clock cycle, scale out more efficiently, and do the same amount of work while moving data less.

Built from the ground up to deliver the best performance-per-watt for HPC and Big Data workloads, the Alveo™ U55C accelerator card delivers the efficiency and scalability called for by the most demanding applications. The U55C delivers dense compute and HBM, with onboard 200Gbps networking enabling massive scale-out using Xilinx's groundbreaking open-standards based clustering.

ALVEO ADVANTAGES

Built around Xilinx's powerful Virtex™ XCU55 UltraScale+ FPGA, the Alveo U55C card delivers blazing fast application acceleration. The U55C harnesses the power of Xilinx Adaptive Computing to deliver extraordinary performance unmatched by competing architectures, with:

Data pipeline hyperparallelism	Superior memory management	Optimized data movement
+	+	+

KEY FEATURES

> High-performance, low power High Bandwidth Memory (HBM2)

> High performance-per-watt

> Fast and easy clustering

> Onboard 200 Gbps networking

> Single-slot, small form factor design

Features	Alveo U55C Card
Architecture	Virtex UltraScale+
LUTs	1,304K
Registers	2,607K
DSP Slices	9,024
Form Factor	Single Slot, Full Height, Half Length
Micro-USB	Yes
HBM Memory	16GB
HBM Bandwidth	460 GB/s
N/W Interface	2 x QSFP28 ports
PCI Express	PCIe Gen3 x 16, 2 x PCIe Gen 4x8
Thermal Solution	Passive
Power (TDP)	115W
Tool Support	Vitis™

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Adaptable. Intelligent.

## HPC CLUSTERING FOR EVERY DATA CENTER

Xilinx U55C customers can scale-out their deployment across hundreds of Alveo U55C cards with Xilinx's standards-based, API-driven HPC clustering solution. Alveo accelerators can cluster over a data center's existing infrastructure and network, with lossless performance requiring no proprietary hardware, while eliminating vendor lock in.

Scale Out Architecture on RoCE v2 and DCBx with **existing data center server infrastructure**

Shared workload and shared memory **across multiple cards**
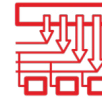
MPI enables hyper-parallelism of Xilinx Adaptive Compute across nodes

HIGH LEVEL PROGRAMMABILITY WITH VITIS™

Xilinx's Vitis Unified Software Platform targets the U55C with high high-level programmability of both the application and cluster, enabling a world of developers and data scientists to unlock the performance benefits of Xilinx Adaptive Computing.

Vitis includes an extensive set of open-source, performance-optimized libraries that offer out-of-the-box acceleration with minimal to zero-code changes to your existing applications.

Vitis HPC Clustering Libraries

> Ease of programming

> Custom data movement

> Write your application in C/C++

Customer Built Applications — C, C++, python™, P4

HPC Clustering

Vitis AI — TensorFlow, Caffe, PyTorch — AI/ML

Video Streaming SDK — FFmpeg, gstreamer — Video transcoding

Graph analytics Libraries | Storage Libraries | Fintech Libraries | Networking Libraries | ERNIC | XNIK

Compilers ARM, HLS, AI Engines, P4 | Analyzers | Debuggers

Vitis runtime (XRT)

Alveo

TAKE THE NEXT STEP

Alveo U55C Accelerator Card.

High Performance Computing Libraries

Database Analytics Libraries