



C-J Alibaba Cloud

Xilinx Powers Alibaba Cloud FaaS with Al Acceleration Solution for E-Commerce Business

Delivers 75% TCO Savings without Compromising Accuracy

AT A GLANCE:

Customer: Alibaba Cloud, a subsidiary of Alibaba Group

Industry: Public cloud service provider

Founded: 2009

Employees: 100,000+

www.alibabacloud.com



CHALLENGE:

China is home of the world's largest online retail market and Alibaba is China's largest e-commerce company. The amount of product images that Alibaba Cloud handles from its many 3rd party vendors is staggering. In order to maintain a consistent experience on their e-commerce sites, oversight on images is required and results in a huge Al inference compute workload. To reduce operational expenses, Alibaba Cloud was seeking alternative, cost-effective processor solutions to detect harmful or un-wanted text information embedded in tens of millions of images every day.

SOLUTION:

Xilinx 16nm Virtex UltraScale+[™] FPGA powered Alibaba Cloud FaaS and Xilinx Vitis AI development kit (formally called MLSuite).

RESULTS:

Achieved 75 percent savings in total cost of ownership without compromising accuracy. A single Xilinx UltraScale+ FPGA delivers hundreds of pictures per-second, representing a 3.5X performance improvement over initial GPU implementation.



CHALLENGE:

Massive AI Workloads to Detect Harmful Images

Alibaba Cloud, the cloud computing and data intelligence arm of Alibaba Group, is the No.1 public cloud service provider in Asia Pacific per market share. Alibaba Cloud provides a comprehensive suite of global cloud computing services to power both international customers' online businesses and Alibaba Group's own e-commerce ecosystem.

Alibaba Cloud heterogeneous computing FPGA-as-a-Service (FaaS) platform runs a large-scale FPGA instance, the F3 instance, based on Xilinx 16nm Virtex UltraScale+ VU9P FPGA to support customers inside and outside Alibaba Group.

A large portion of today's internet traffic consists of images. Some images contain harmful unwanted text information such as unpaid advertisements, which have negative impacts on the paid advertisement business. In order to maintain a consistent experience on e-commerce sites, oversight on images is required and creates a large AI inference compute workload.

SOLUTION:

Directly Accelerate Yolo-v2 on Xilinx FPGAs from ML Framework

Alibaba historically used GPUs to run Yolo-v2 Tiny with Float32 data type in order to understand the content in tens of millions of images every day. As the architecture was not well optimized, the GPU could only achieve limited queries per second (QPS) throughput, which resulted in very high costs in power and server footprint. To reduce operation expenses, Alibaba looked for a more cost-effective solution than GPUs for detecting harmful or un-wanted text information.

With the highly adaptable architecture of Xilinx FPGAs, the Alibaba Cloud FaaS team ran the Yolo-v2 Tiny model at Int16 to achieve superior QPS performance with similar accuracy to GPUs. Inspired by FaaS, with the similar optimization, GPU can achieve similar QPS; however, the Xilinx solution is much more cost effective per image because the GPU solution has a much higher TCO. In this project, the Alibaba FaaS team also used Vitis AI to expedite their development.



VU9P Vs GPU for YOLO V2 Tiny

User Application	
Caffe	1 TensorFlow
Model Zoo	Custom Models
Al Compiler Al Quantizer Al Optimizer	
Al Profiler Al Library	
Xilinx Runtime library (XRT)	
Deep Learning Pro	cessing Unit (DPU)
	User Application Caffe Model Zoo Al Compiler Al Quant: Al Profiler J Xilinx Runtim Deep Learning Pro

Xilinx Vitis AI (formally MLSuite)

AMDA XILINX

RESULT

Delivers 75% TCO Savings without Compromising Accuracy

Vitis AI allows developers to optimize and deploy pretrained DNN models to the Xilinx FPGA without writing any RTL code. The runtime and shell allow them to benefit f om Xilinx hardware acceleration, without needing to be an FPGA expert.

Mr. Jeff Zhang, Director of Alibaba Cloud FaaS platform, who led the project and successfully implemented AI acceleration into F3 instance, said: "Alibaba Cloud FaaS provides a unified ha dware platform and

"Thanks to MLSuite (now part of Vitis AI), the beauty of how Alibaba and Xilinx developed AI acceleration solutions is that no one at Alibaba had to become an FPGA expert to use the technology."

- Mr. Zhang

middleware in the cloud. With the support from Xilinx Vitis AI, Alibaba FaaS can significantly educe development and deployment costs of AI accelerators. Accelerator vendors can provide accelerators as a service to users, eliminating the hardware barriers of acceleration technology. Users can use the acceleration services on demand without having to understand underlying hardware details."

Mr. Zhang also pointed out: "At the beginning, many people were not optimistic about the prospect of FPGA in the field of AI. GPU is convenient to use and supports all frameworks. The success of this project proves that FPGA is quite suitable for specific scenarios in this field, and in p ticular, it has considerable cost-effective advantages for cloud AI inference. For example, shells on the cloud make development much easier; low width and pruning significantly educes cost and power; IP such as image sharpening, FFT filters bring ext a value to some innovative applications

CONCLUSION

Overall, Alibaba Cloud is pleased with Xilinx and believes "through the FaaS platform, together with the vast number of ISV and independent IP developers, FPGA has a great opportunity in the Al inference in the cloud." - Mr. Zhang

Additional Resources:

Learn More about Vitis AI | Learn more about Virtex UltraScale+ FPGAs | Xilinx Blog | XDF Keynote

Corporate Headquarters Xilinx, Inc. 2100 Logic Drive San Jose, CA 95124 USA Tel: 408-559-7778 www.xilinx.com Xilinx Europe One Logic Drive Citywest Business Campus Saggart, County Dublin Ireland Tel: +353-1-464-0311 www.xillinx.com Japan Xilinx K.K. Art Village Osaki Central Tower 4F 1-2-2 Osaki, Shinagawa-ku Tokyo 141-0032 Japan Tel: +81-3-6744-7777 japan.xilinx.com Asia Pacific Pte. Ltd. Xilinx, Asia Pacific 5 Changi Business Park Singapore 486040 Tel: +65-6407-3000 www.xilinx.com India Meenakshi Tech Park Block A, B, C, 8th & 13th floors, Meenakshi Tech Park, Survey No. 39 Gachibowli(V), Seri Lingampally (M), Hyderabad -500 084 Tel: +91-40-6721-4747 www.xilinx.com

© Copyright 2021 Advanced Micro Devices, Inc. All rights reserved. Xilinx, the Xilinx logo, AMD, the AMD Arrow logo, Alvoo, Artix, Kintex, Kria, Spartan, Versal, Vitis, Virtex, Vivado, Zynq, and other designated brands included herein are trademarks of Advanced Micro Devices, Inc. Other product names used in this publication are for identification purposes only and may be trademarks of their respective companies. AMBA, AMBA Designer, ARM, ARM1176JZ-S, CoreSight, Cortex, and PrimeCell are trademarks of ARM in the EU and other countries. PCIe, and PCI Express are trademarks of PCI-SIG and used under license. Printed in the U.S.A. WW118-21