PRODUCT ADVISORY
Alveo U30 Media Accelerator Card

Limitations – June 2020

The Alveo U30 Media Accelerator Card is designed to be used in a Xilinx Real-Time video appliance or turn-key High-Density video transcoding appliance built by its VARs. The following is a list of some but not all limitations for this product.

Area	Support	Notes
Vivado Tools	No support	There are no design examples, reference designs, Vivado board or xdc files available or planned to target Zynq Ultrascale Plus devices on the Alveo U30 card.
Server	Bifurcation	The card must be hosted in a server that supports bifurcated Gen3 x8 to form two independent Gen3 x4 PCI Express® (PCIe) compliant interfaces to target two Zynq MPSoC Ultrascale+ devices
PCIe	Boot time specification	As the card is targeted for custom server appliance, it has not been designed to meet standard PCI Express® boot time specification which requires the PCIe® link to be ready to link train with a peer within 120 ms after power is stable
Vitis Platform	Vitis Compilation	There are not Vitis Platform deliverables available for kernel compilation, the Alveo U30 provide a fixed Video Transcode solution from Xilinx.

Please work with your Xilinx Sales representative to understand details around the above limitations and to explore further Alveo U30 capabilities as documented in the product webpage www.xilinx.com/u30