Victor Peng

President and Chief Executive Officer, Xilinx

Victor Peng is President and Chief Executive Officer of Xilinx. He also serves as a member of the Board of Directors of Xilinx. Peng has over 30 years of experience defining and bringing to market leadership FPGAs, SoCs, GPUs, high performance microprocessors and chip sets, and microprocessor IP products.

Since becoming CEO of Xilinx earlier this year, Peng has rolled out plans for a transformation to address new markets with new technology, underpinned by the Adaptive Compute Acceleration Platform (ACAP). In his previous role as Chief Operating Officer, Peng managed Global Sales, Product & Vertical Marketing, Product Development, and Global Operations & Quality. Prior to that, he served as Executive Vice President & General Manager of Products at Xilinx, and led the definition, development and product marketing of the company's portfolio of products and differentiated technologies – resulting in three consecutive generations of core product leadership and significant industry breakouts in integration and programming.

Before joining Xilinx, Peng served as Corporate Vice President of the graphics products group (GPG) silicon engineering with AMD, where he also served as a key leader for AMD's central silicon engineering team supporting graphics, console game products, CPU chipset and consumer business units. Prior to that, Peng held executive and engineering leadership roles at TZero Technologies, MIPS Technologies, SGI and Digital Equipment Corp.

Peng earned a BSEE from Rensselaer Polytechnic Institute and an M.Eng. in Electrical Engineering from Cornell University. He holds four U.S. patents.

Liam Madden

Executive Vice President,
Hardware and Systems Product Development, Xilinx

Liam Madden is Executive Vice President of Hardware and Systems Product Development at Xilinx. He leads a global team of about 1,000 engineers and oversees the strategy, design solutions, priorities and execution specific to the growth of all Xilinx Hardware and System solutions, including silicon, packaging,

evaluation systems and embedded software. Madden led the development of the highly successful 28nm, 20nm and 16nm families. He was responsible for the introduction of stacking technology at 28nm, leading the industry in integrating multiple devices on a silicon interposer for which he received the 2013 Semi Award. He is responsible for integrating Gb/s data converters into Xilinx products as part of the RFSOC product line aimed at revolutionizing 5G radio solutions and related products.

Madden is a nine year veteran of Xilinx. In over a 35 year career, he has contributed to a range of industry-leading products, including: high performance and low power microprocessors (Alpha and StrongArm at DEC), embedded processors and IP (MIPS) and consumer devices (Xbox 360 at Microsoft).

Prior to joining Xilinx, he was a Senior Fellow at AMD where he drove AMD's next generation chip integration methodology.

Madden holds a BE degree from UCD, and an M.Eng. degree from Cornell University. He is an Adjunct Professor at UCD, a Fellow of the Institute of Engineers Ireland and a Board Member of Science Foundation Ireland.

Salil Raje

Executive Vice President,
Software and IP Products Group, Xilinx

Salil Raje is Executive Vice President of Software and IP Products Development. He leads a global team of about 1,000 engineers and oversees the strategy, design solutions, priorities and execution specific to the growth of Xilinx Software and IP. Raje led the introduction of the Vivado Design Suite in 2012 – a grassroots effort to modernize the design flow that resulted in Xilinx achieving design tools and IP leadership and improved customer productivity. He is also responsible for leading initiatives around Vivado HLx and SDx development environments that allow customers to adopt higher-level abstraction flows and are instrumental in the company's expansion efforts in the data center market, machine learning and vision-based applications.

Raje is a 13-year veteran of Xilinx. Prior to joining Xilinx, he was CTO, then CEO, of Hier Design Inc. which he co-founded in 2001. At Hier Design, he was responsible for introducing a hierarchical design and design planning tool, PlanAhead, to the FPGA design community. While CEO, he oversaw the acquisition of Hier Design by Xilinx Inc. in 2004.

Prior to Hier Design, Raje was a Director at Monterey Design Systems responsible for placement and silicon virtual prototyping technology. He started his career at IBM T. J. Watson Research Center in Yorktown Heights, New York working in High Level Synthesis.

Raje holds a Bachelor of Technology in Electrical Engineering from IIT, Madras and a Master of Science and a Ph.D. in Computer Science from Northwestern University. He holds eight patents in the areas of electronic design tools, ASIC and FPGA designs, and has authored over 15 industry recognized research papers.

Emre Onder

Senior Vice President, Marketing, Xilinx

Emre Onder is Senior Vice President of the Product and Vertical Marketing group at Xilinx, including portfolio and product planning, product marketing and vertical marketing. Onder has more than 25 years of experience as a General Manager leading global organizations in cross-functional disciplines including marketing, strategic alliances, channel and strategic account sales, engineering and new product development. Onder has a track record of creating growth in large and mature businesses as well as complex, emerging markets.

Before joining Xilinx, Onder served as Vice President, Global Sales and General Manager for Honeywell Sensing & IoT. Previously, he served as Vice President of Marketing and Core Markets at Analog Devices where he grew the company's broad-market segment business to over $1.2 billion annual revenue by creating innovative solution platforms jointly with Xilinx. He also led ADI's new global distribution and award-winning digital strategy. Earlier in his career, Onder worked at EMC Corporation and the Boston Consulting Group.

Onder earned an MBA and Bachelor's Degree in Electrical Engineering from Stanford University. He is a recipient of the F.E. Terman Engineering Award.

Manish Muthal

Vice President, Data Center, Xilinx

Manish Muthal is Vice President, Data Center at Xilinx. He is responsible for driving Xilinx's Data Center business with Hyperscale, HPC and Enterprise customers across applications like machine learning, databases, big data analytics, video transcoding and storage/network acceleration.

He was previously Vice President of Product Marketing at Broadcom, responsible for Broadcom's multi-core MIPS and ARM64 processors for communications, storage, and server applications. Prior to Broadcom, Muthal was Vice President of Strategic Planning, Marketing and Architecture at Seagate and LSI, responsible for their Flash product strategy and business execution. He is a seasoned executive with

business strategy, marketing and technology domain experience across compute, networking and storage substrates within Hyperscale Data Center, Enterprise Data Center and Carrier Infrastructure.

Muthal earned a MS in Computer Engineering from UC Santa Barbara, and a BE in Electronics and Telecommunications from the Visvesvaraya National Institute of Technology, India.

Kirk Saban

Senior Director of Product & Technical Marketing, Xilinx

Kirk Saban is Senior Director of Product & Technical Marketing at Xilinx. He is responsible for product and technical marketing for Xilinx's FPGAs, SoCs, Boards and Kits and its new product category called Adaptive Compute Acceleration Platform (ACAP). He also oversees global customer training, sales training and sales enablement.

Saban has been with Xilinx for more than 13 years, joining in 2005 as a Field Applications Engineer.

Prior to joining Xilinx, he was a Development System Specialist with Canadian-based RTDS Technologies Inc., where he designed and developed various interface cards utilizing FPGA and DSP technology.

Saban holds a B.Sc. (EE) in Electrical Engineering from the University of Manitoba in Canada.

Harpinder Matharu

Director, Communications Business, Xilinx

Harpinder Matharu is a Director for the Communications Business at Xilinx where he manages the strategic and technical marketing team. His current focus area is Cloud RAN, 5G and SDN/NFV applications for mobile networks.

Previously at Xilinx, Matharu managed connectivity, radio, switching and packet processing products for wireless infrastructure and backhaul.

He has more than 25 years of experience working in different capacities within the communications industry. Matharu has published many papers, spoken at industry events, and chaired standards technical and marketing forums.