

Design Services Partner Profile

Xylon d.o.o. Company Overview

Date : 05/17/2018

www.logicbricks.com

Partner Details	Partner Tier: Premier		
	Headquarters Location: Fallerovo Setaliste 22,Zagreb,,Croatia		
	Xilinx Alliance Partner Since: 4/11/2000		
Key Contact Info	Ivana Vojnovic ivana.vojnovic@logicbricks.com		
Regions Served	APAC, Africa, Central and South America, Eastern Europe and Russia, India, Japan, Middle East, North America Central, North America East, North America West, Western Europe		
Supported Product Types	Yes Design Services	Yes Embedded Products	
	Yes IP	No EDA Tools	
	Yes Boards		
Engineering Service Types	Yes General Purpose Services	Preferred	
	Yes Services in Support of Products Sold		
	Yes Services Leading to Contract Manufacturing		
	Maximum Service Scope: Turnkey / full product design		
Engagement Models Supported	Yes SOW-Based Fixed Bid Projects	Preferred	
	Yes Time and Materials		
	Both Captive Resources at Partner or Customer Premises		
Xilinx Design Tool Experience	Yes Vivado	Yes SDSoC	
	Yes HLx	No SDAccel	
Industry Certifications Achieved	ISO 9001		
Industry Certification Services Offered			
Government Clearances	None		

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Xylon d.o.o. Segment and Application Focus

Primary Focus	Defense/Aerospace	No Focus No Focus Primary Focus Secondary Focus No Focus Primary Focus No Focus No Focus No Focus Primary Focus	Homeland Security Electronic Warfare ISR Space Missiles & Munitions Military Avionics Commercial Avionics SatComm Wireless Milcom Other A&D
Secondary Focus	Audio, Video, Broadcast	Secondary Focus Primary Focus Primary Focus Secondary Focus No Focus Secondary Focus No Focus Primary Focus No Focus	Encoders & Decoders Monitors & Projection Networking & Converters Audio Systems Video Processing Cards Video Conferencing Routers and Switches Pro Cameras & Camcorders Other Broadcast
Primary Focus	Automotive & Transport	Primary Focus Secondary Focus Secondary Focus Primary Focus Primary Focus	Driver Assist - Camera Driver Assist Radar/LIDAR Driver Info/Infotainment Automated Driving Other Automotive
Secondary Focus	Consumer	Secondary Focus Secondary Focus Secondary Focus No Focus	Digital Display Drones Multifunction Printers Other Consumer
No Focus	Datacenter	No Focus No Focus No Focus	Storage Compute Networking
Primary Focus	Industrial, Medical	No Focus No Focus Primary Focus Primary Focus Primary Focus Primary Focus Secondary Focus	Motor Control Industrial Networking Surveillance Machine Vision Medical Imaging Medical / Clinical Other ISM

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Xylon d.o.o. Segment and Application Focus (continued)

Secondary Focus	Test & Measurement	<ul style="list-style-type: none"> Secondary Focus Primary Focus Secondary Focus No Focus No Focus No Focus 	<ul style="list-style-type: none"> Simulation / Emulation T & M Instrumentation Semiconductor Test Wired Communication Test Wireless Communications Test Other Test & Measurement
No Focus	Wired Comms & Networks	<ul style="list-style-type: none"> No Focus No Focus No Focus No Focus No Focus No Focus No Focus No Focus 	<ul style="list-style-type: none"> Access Equipment Long Haul/Transport Wired Mobile Backhaul OTN Enterprise Equipment Ethernet Interlaken Connectivity Packet Processing Other Wired Communications
No Focus	Wireless Comms & Networks	<ul style="list-style-type: none"> No Focus No Focus No Focus No Focus No Focus 	<ul style="list-style-type: none"> Baseband Wireless Backhaul Connectivity & Switching Radio Other Wireless

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Design Competencies

System Architecture	Primary Focus	UFDM certified, HW/SW partitioning, soft-CPU's and SoC/MPSoC CPU's, optimizations on size and timings, system modeling
RF and Analog Design	Secondary Focus	Power supplies, eventual ADCs, PHYs (i.e. MIPI), power supply monitors, video front-ends with operational amplifiers
FPGA Design and Integration	Primary Focus	Xylon works exclusively with Xilinx devices since 1995. We have used almost all FPGA and SoC families, starting from the XC3000.
FPGA Timing Closure	Primary Focus	Familiar with all Xilinx design guidelines for timing constraining, static timing analysis, and all techniques to achieve and timing goals.
FPGA Design Optimization	Primary Focus	Familiar with all techniques and coding styles (including partial reconfiguration) that assure the best optimization and timing closure results in different families.
Signal Integrity Design and Analysis	Secondary Focus	With Xilinx developed a proprietary Gbps multimedia serial link (VLINK) that works through an ordinary differential pair. Experienced with FPD-Link II, FPD-Link III, APIX, APIX2, Ethernet, MIPI, HiSpi, SATA, MOST....
	Inhouse or Subcontracted: Inhouse Typical Serial Speed: 3	
PCB Design & Layout	Primary Focus	Able to produce complex PCBs: BGA IC footprints, Gbps lines, up to 20 layers...Connections to different Mfg. Do PCB designs for customers within proof-of-concept and demo projects, as well as small volume Xylon HW products.
	Inhouse or Subcontracted: Inhouse	
Industrial and Mechanical Design	Secondary Focus	Xylon can design small mechanical products in order to complete in-house or customer developed electronics.
	Inhouse or Subcontracted: Both	
Industry Certification and Compliance Testing	Secondary Focus	Preparation of Xylon products for CE and FCC conformance testings.
	Inhouse or Subcontracted: Subcontracted	
Quality and Reliability Testing	Secondary Focus	Experienced in using Xilinx Soft Error Mitigation (SEM) IP core and some other Xilinx functional safety tools.
	Inhouse or Subcontracted: Inhouse	
Contract Manufacturing	Secondary Focus	Xylon has developed a number of different Xilinx-based development boards, FMC cards, camera modules and also can manufacture different add-on cards (mostly FMC) to support customer prototyping needs. Supported Xilinx TDPs,
	Inhouse or Subcontracted: Subcontracted Max Quantities Supported: 1000s	

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Digital Signal Processing Experience:	Primary Focus MicroBlaze, Zynq SoC and Zynq UltraScale MPSoC	Specialists for video and computer vision, DSP, algorithm development and optimizations for use in Xilinx devices.
Embedded Processors Experience:	Primary Focus PicoBlaze, MicroBlaze, Zynq, PowerPC, MIPS	Experienced with Xilinx soft-CPU's, PowerPC and ARM processing systems, as well as with many other embedded SoCs (Intel, MIPS, ARM...)
Embedded Porting / BSP Extension OS and RTOS Design Experience:	Secondary Focus Android, Linux, QNX, Windows Embedded	Xylon provides software drivers for logicBRICKS IP cores and can develop software applications for different operating systems.
Embedded Application Development	Primary Focus	Xylon is capable to develop embedded applications for WEC OS, Linux OS, no-OS or RTOS.
Image Sensor Fusion Homo or Heterogeneous: Sensor Types Design Experience:	Primary Focus Both Accelerometer, Compass, GPS / Position, Gyro, Inclinometer, Other / Contact Partner, Thermal, Tilt, Video (SD, HD)	Complete automotive surround view system solution. Customers are major OEMs and Tier1s. Heterogeneous video/thermal sensor fusion demo. Heterogeneous video/IMU units (accelerometer, gyroscope and magnetometer).
Computer Vision	Primary Focus	Xylon provides solutions for face detection and tracking, emotions, gender and age estimators, driver monitoring systems, objects detections and recognition...
Machine Learning Framework Experience: Network Model Experience:	Primary Focus Caffe AlexNet, FCN, GoogLeNet, ResNet, SSD, SqueezeNet, VGG	Xylon is an active member of the reVISION ecosystem, and works with the complete stack.